CPU 102

MCH 106

MEMORY 104

HUBLINK

BIOS 112

BUS INTERFACE 110

PCI 114

PCI DEVICE 116

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HOST INTERFACE 118

130

FIFO 122

MAC 124

132

RxD CLK TxD RESET/SYNC

134 138 136 139

128

PHY 126

DATA LINK

POINTER 140

FIG. 1

	0	1	2	3	4	5	6 7	8	9	10	11
Word 0		Rx_Er		CRS	Even	PtM_mode	00	Mdout	Tx_Cyc	Rx_Cyc	Rx_DV = 0
Word 1	I_Er	Seed	Duplex	CRS	SQL	RST_RQST	01	Mdout	Tx_Cyc	Rx_Cyc	Rx_DV = 0
Word 2	Int rqst	Link	rsrvd	CRS	rsrvd	rsrvd	10	Mdout	Tx_Cyc	Rx_Cyc	Rx_DV = 0

FIG. 2

0	Rdata7
1	Rdata6
2	Rdata5
3	CRS
4	Rdata4
5	Rdata3
6	Rdata2
7	Rdata1
8	Rdata0
9	Tx_Cyc
10	Rx_Cyc = 1
11	Rx_Dv = 1

FIG. 3

0	1	2	3	4	5	6	7	8	9	10	11
Rdata7	Rdata6	Rdata5	CRS	Rdata4	Rdata3	Rdata2	Rdata1	Rdata0	Mdout	Rx_Cyc = 1	Rx_Dv = 1

FIG. 4

0	rsrvd
1	rsrvd
2	rsrvd
3	CRS
4	rsrvd
5	rsrvd
6	rsrvd
7	rsrvd
8	Mdout
9	Tx_Cyc
10	Rx_Cyc = 0
11	Rx_Dv = 1

FIG. 5

11	10	9	8	7	6	5	4	3	2	1	0
SEL = 0	Mdstart	Mdin	Tx_EN = 1	Tdata0	Tdata1	Tdata2	Tdata3	Tdata4	Tdata5	Tdata6	Tdata7
SEL = 1			Tx_EN = 0	rsrvd	rsrvd	rsrvd	rsrvd	PHY_PD	Lpbk	LED_SEL	Rx_addr _match

Command_Word
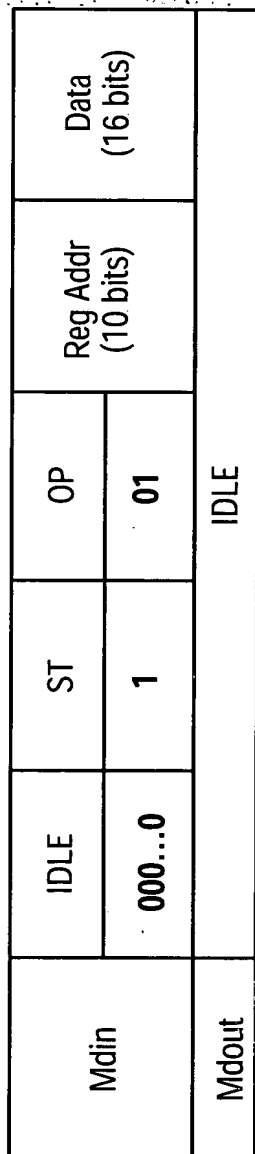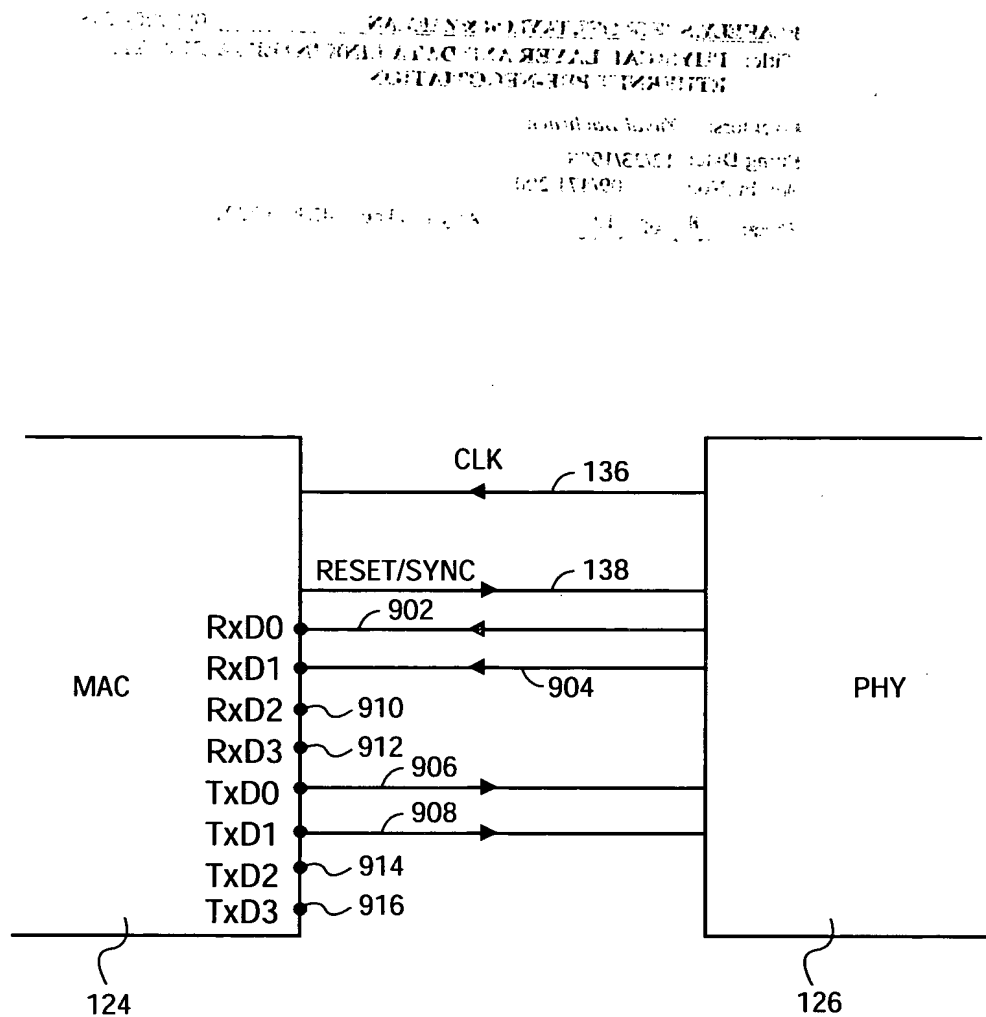
FIG. 6

Mdin	IDLE	ST	OP	Reg Addr (10 bits)	Data (16 bits)
	000...0	1	01		
Mdout			IDLE		

FIG. 7

```
                          CLK          ┌─ 136
                   ◄──────────────────────
┌──────────┐                          ┌──────────┐
│          │      RESET/SYNC   ┌─ 138 │          │
│          │       ──────────────────►│          │
│          │  RxD0  ┌─ 902            │          │
│          │  ●──────────────◄────────│          │
│          │  RxD1          ┌─ 904    │          │
│   MAC    │  ●──────────────◄────────│   PHY    │
│          │  RxD2  ┌─ 910            │          │
│          │  ●─╮              ┌─ 906 │          │
│          │  RxD3  ┌─ 912     │      │          │
│          │  ●─╮        ┌─ 908       │          │
│          │  TxD0 ●──────────────────►          │
│          │  TxD1 ●──────────────────►          │
│          │  TxD2  ┌─ 914            │          │
│          │  ●─╮                     │          │
│          │  TxD3  ┌─ 916            │          │
│          │  ●─╮                     │          │
└──────────┘                          └──────────┘
      └─ 124                                └─ 126
```

FIG. 9
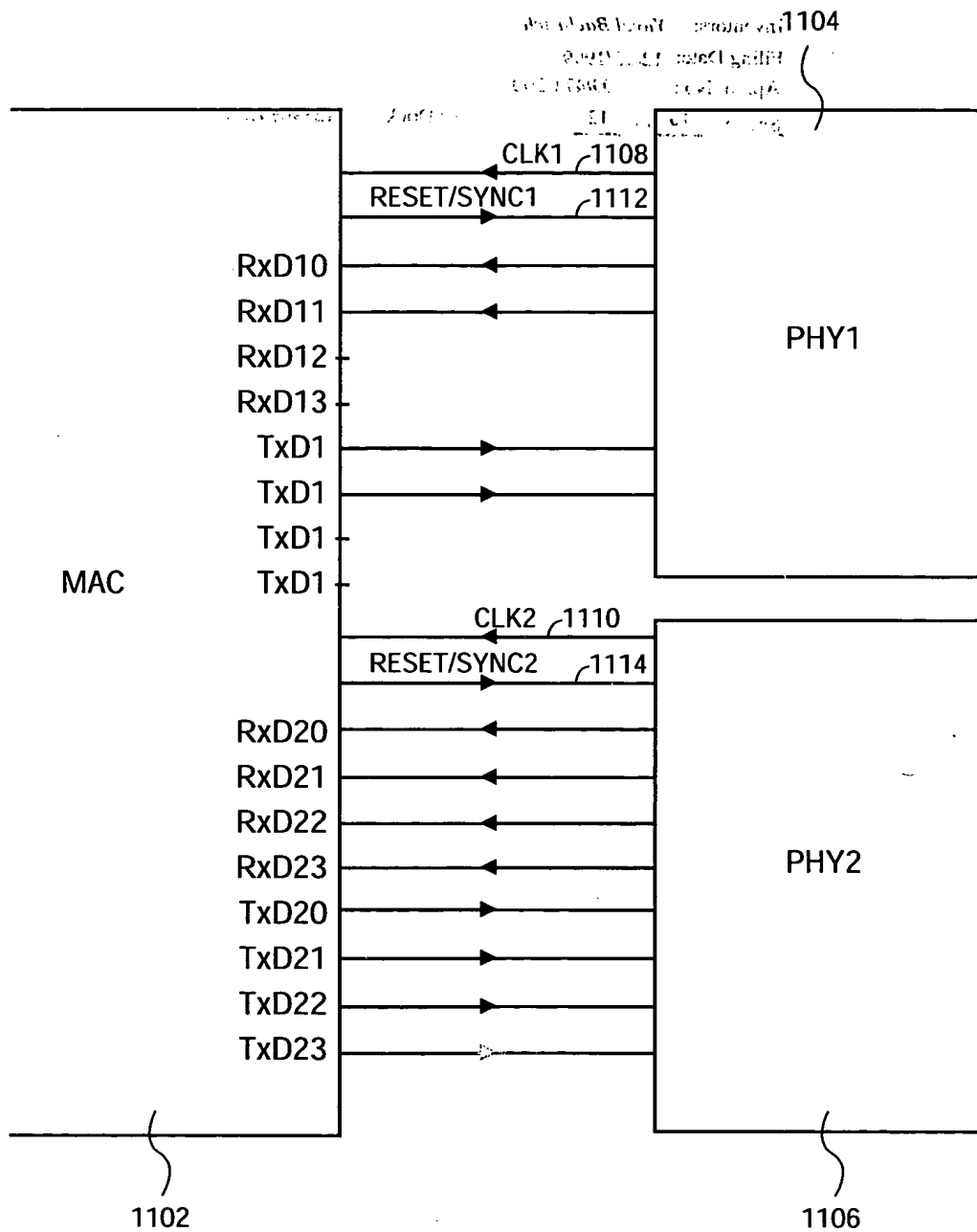
FIG. 10

FIG. 11

FIG. 12A

FIG. 12B

US 094712000CP1
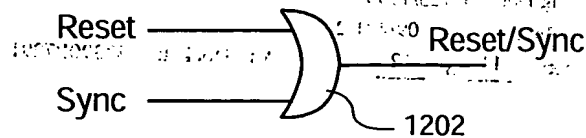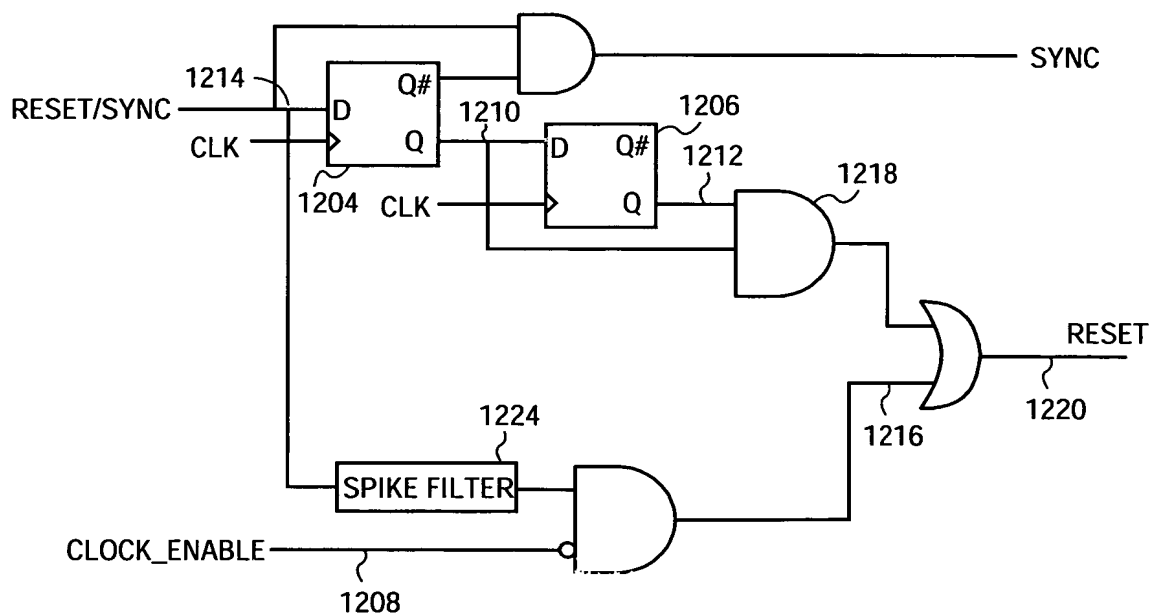

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